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Attorney's Docket No. Micronas 7397



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 7,202,532

Issued: April 10, 2007

Name of Patentee: Czech et al.

Title of Invention: VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA

ATTN: Certificate of Correction Branch

REQUEST FOR CERTIFICATE OF CORRECTION
OF PATENT FOR PTO MISTAKE (37 CFR 1.322(a))

1. Attached, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.

NOTE: Form PTO-1050, using the column and line number in the printed patent, should be used exclusively regardless of the length or complexity of the subject matter. MPEP S 1485.

NOTE: The patent grant should be retained by the patentee. The PTO does not attach the certificate of correction to the patentee's copy of the patent. The patent grant will be returned to the patentee if submitted. MPEP S 1485.

2. The exact page and line number where the errors are shown correctly in the application file are:

On the Front Page

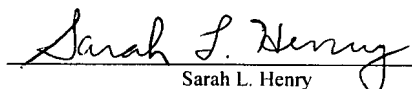
Delete the current title and insert --VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT--

Column 1

Line 1, delete "INTEGRATED CIRCUIT" and insert --VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT--

NOTE: This information should be identified in this request, however, on Form PTO-1050, only the column and line number in the printed patent should be used. MPEP S 1485.

I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being deposited on April 12, 2007 with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Sarah L. Henry

Certificate
APR 18 2007
of Correction

APR 18 2007

3. Please send the Certificate to:

Name: Patrick J. O'Shea, Esq.

Address: O'Shea, Getz & Kosakowski, P.C.

1500 Main Street, Suite 912

Springfield, Massachusetts 01115

Signature(s) of patentee(s)

or

Micronas GmbH

(TYPE OR PRINT NAME OF ASSIGNEE)

Patrick O'Shea

Signature of assignee or person authorized to
sign on behalf of assignee

☐ Assignment recorded on:

Patrick O'Shea, Esq.

(TYPE OR PRINT NAME OF AUTHORIZED
PERSON SIGNING)

Attorney of record

Title of authorized person signing

☐ Recordal of assignment attached

☐ Attached is a "CERTIFICATE UNDER 37 CFR 3.73(b)," establishing the right of the assignee to take action in this case.

NOTE: "A certificate of correction, under 35 U.S.C. 254, may be issued at the request of the patentee or [THE PATENTEE'S] ASSIGNEE." 37 CFR 1.322(A). THE CERTIFICATE OF CORRECTION CAN BE SIGNED BY THE ATTORNEY OF RECORD WHO ACTS ON BEHALF OF THE INVENTOR(S) OR ASSIGNEE(S).

APR 18 2007

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO: 7,202,532
DATED: April 10, 2007
INVENTOR(S): Czech et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Front Page

Delete the current title and insert --VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT--

Column 1

Line 1, delete "INTEGRATED CIRCUIT" and insert --VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT--

MAILING ADDRESS OF SENDER:

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PATENT NO. 7,202,532

APR 18 2007